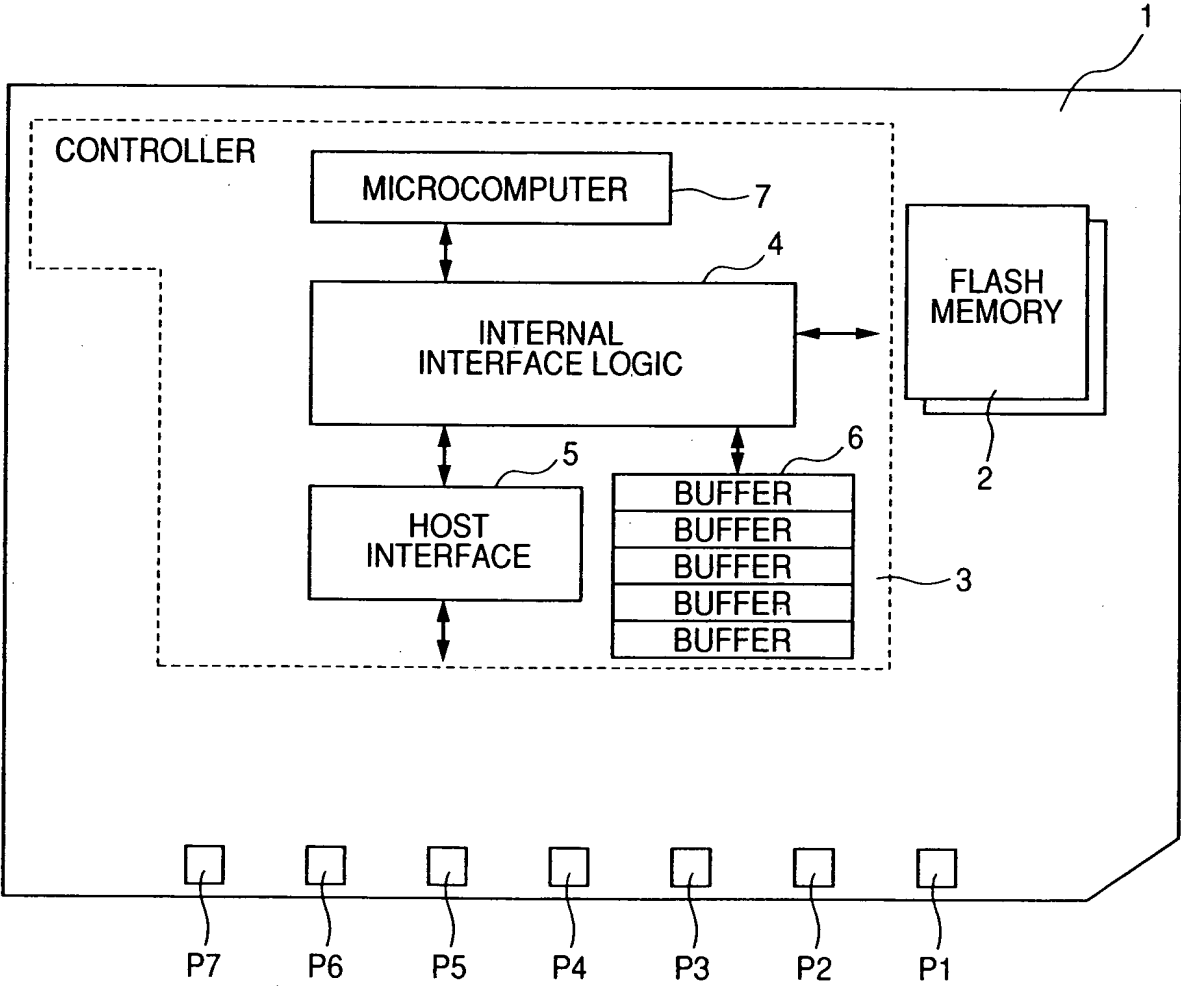


FIG. 1



**FIG. 2**

PIN NO.	DESIGNATION	I/O	DESCRIPTION
1	RESERVED	NC	RESERVED TERMINAL
2	CMD	I/O	COMMAND
3	VSS1	—	GROUND
4	VCC	—	POWER SUPPLY
5	CLK	I	CLOCK
6	VSS2	—	GROUND
7	DAT0	I/O	DATA BUS bit [0]

**FIG. 3**

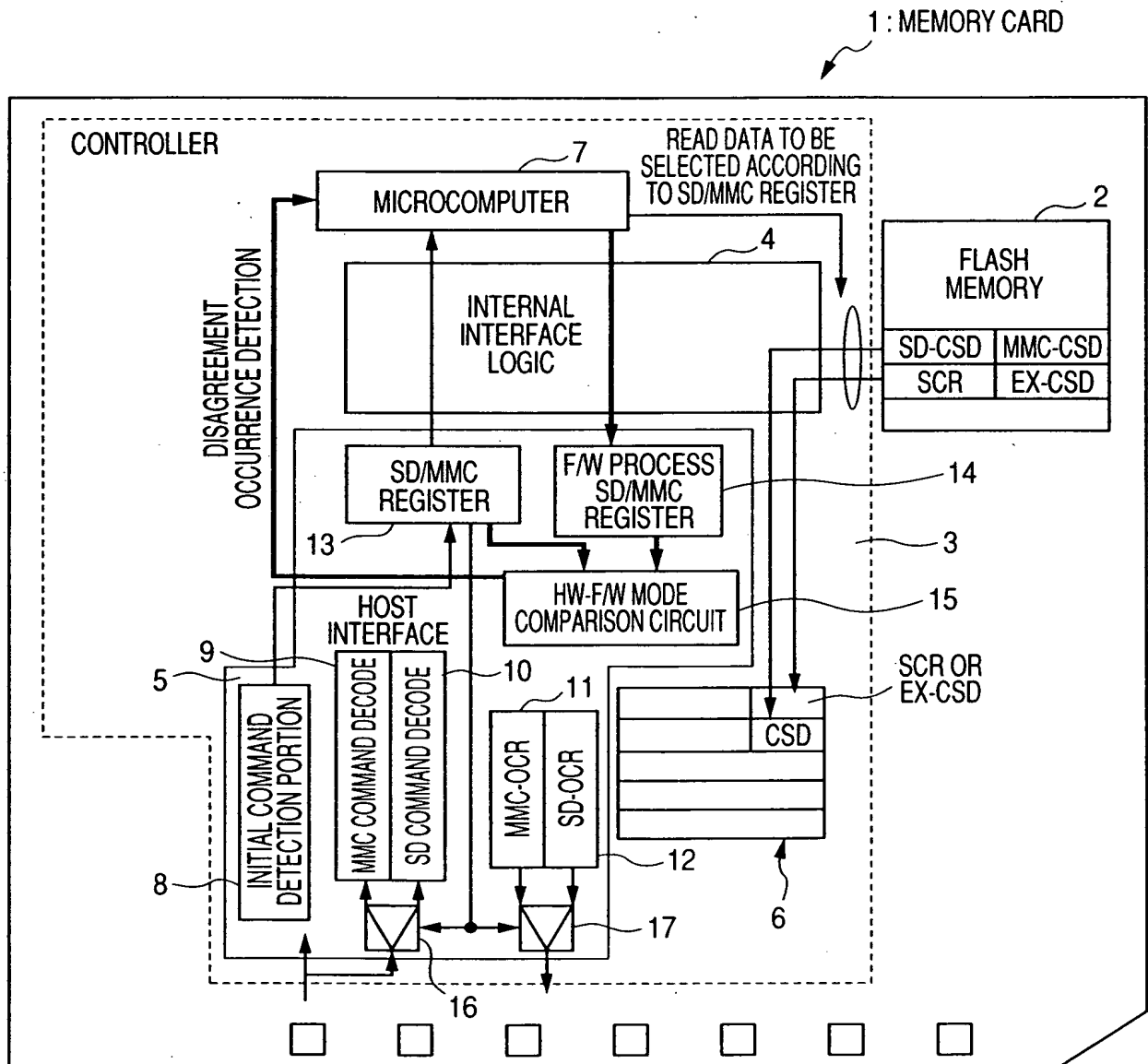
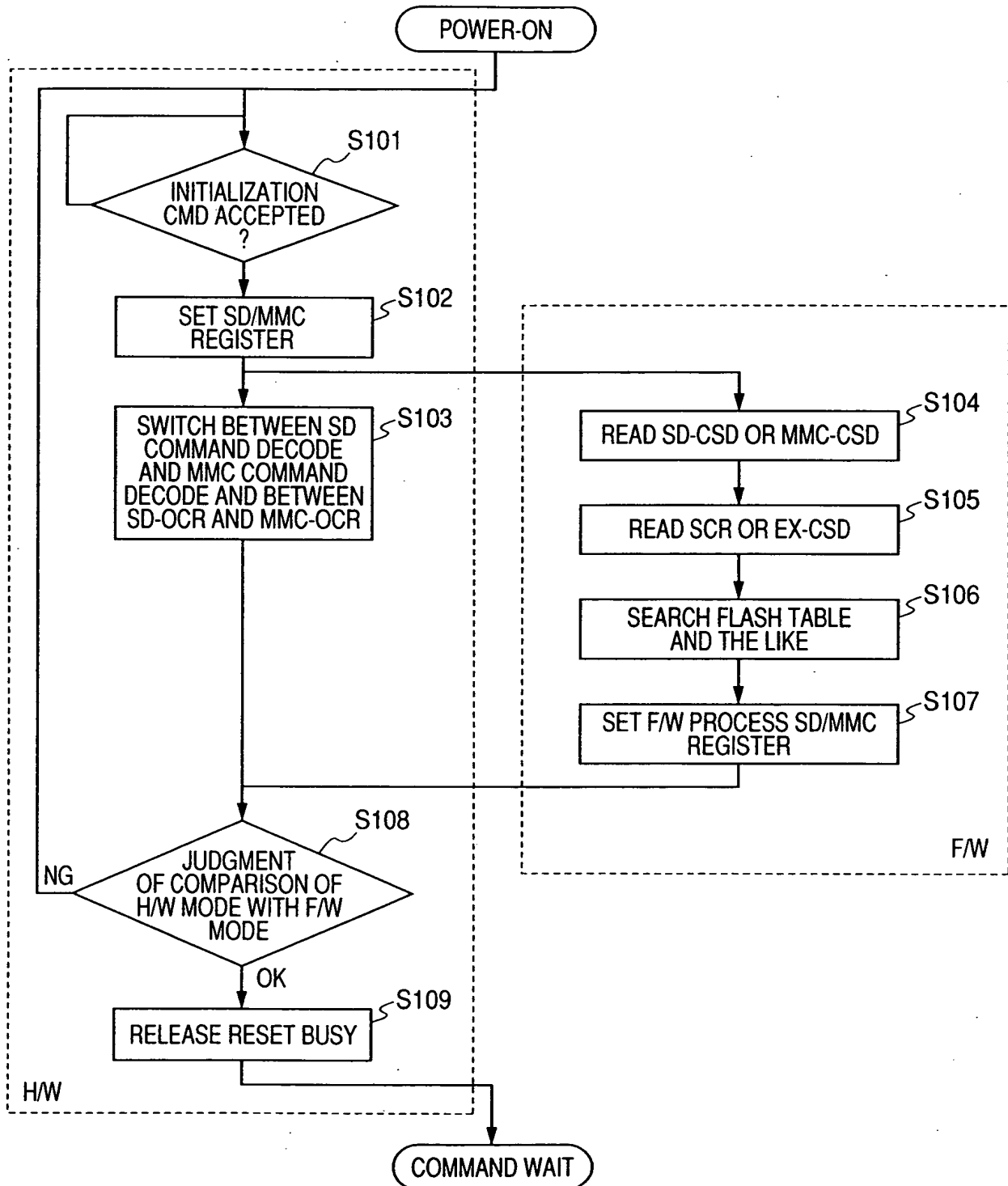


FIG. 4



**FIG. 5**

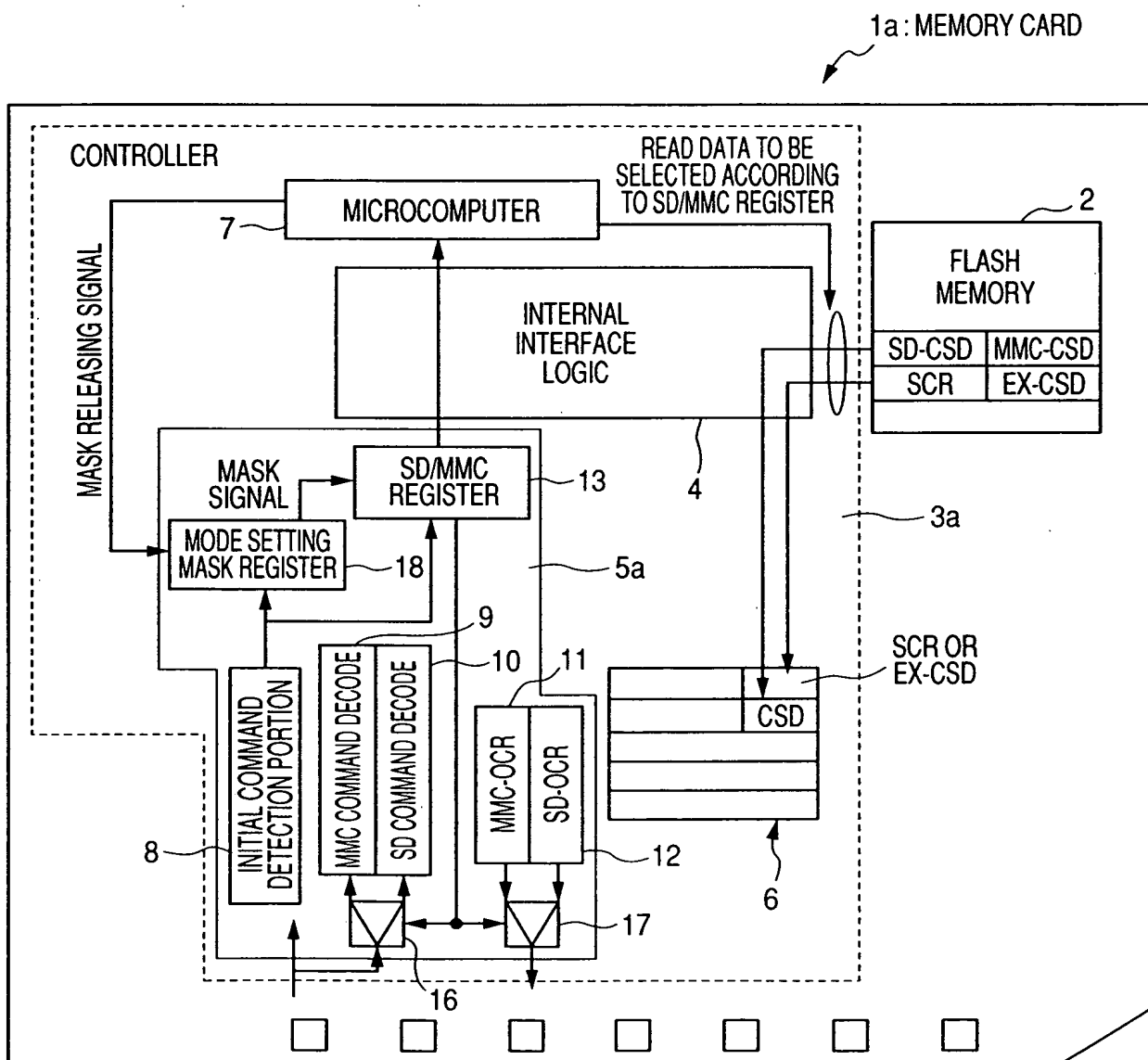


FIG. 6

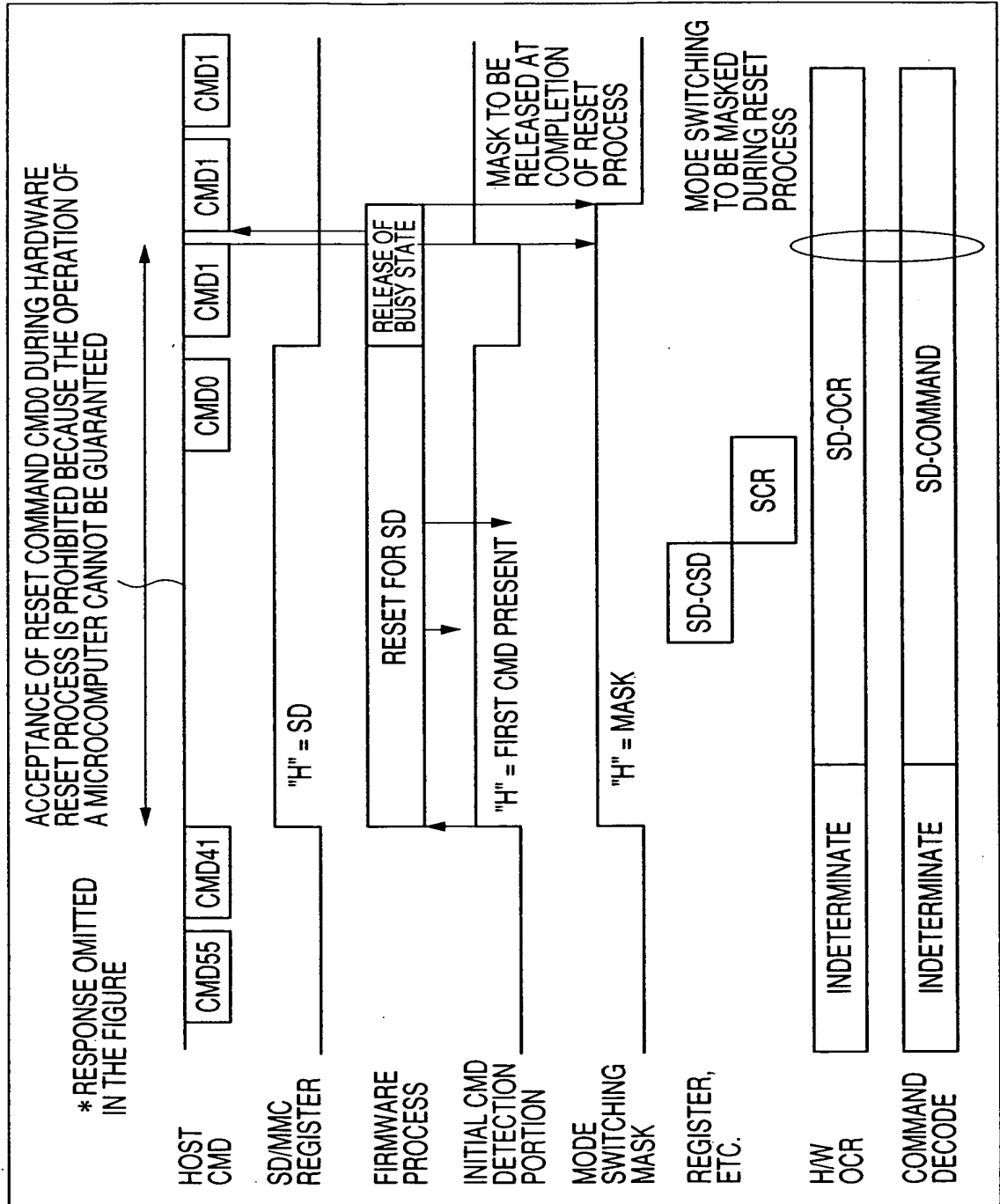
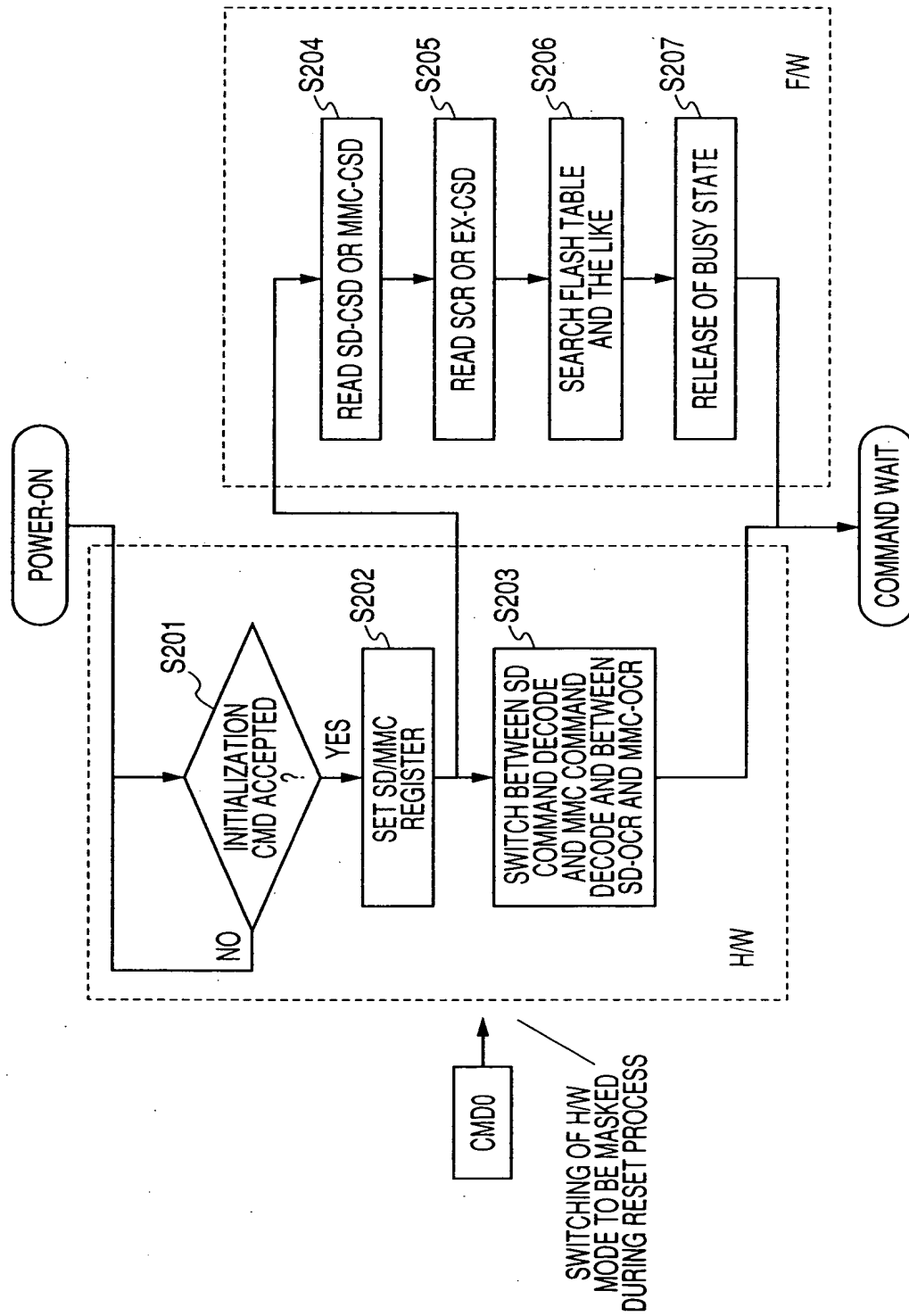


FIG. 7



**FIG. 8**

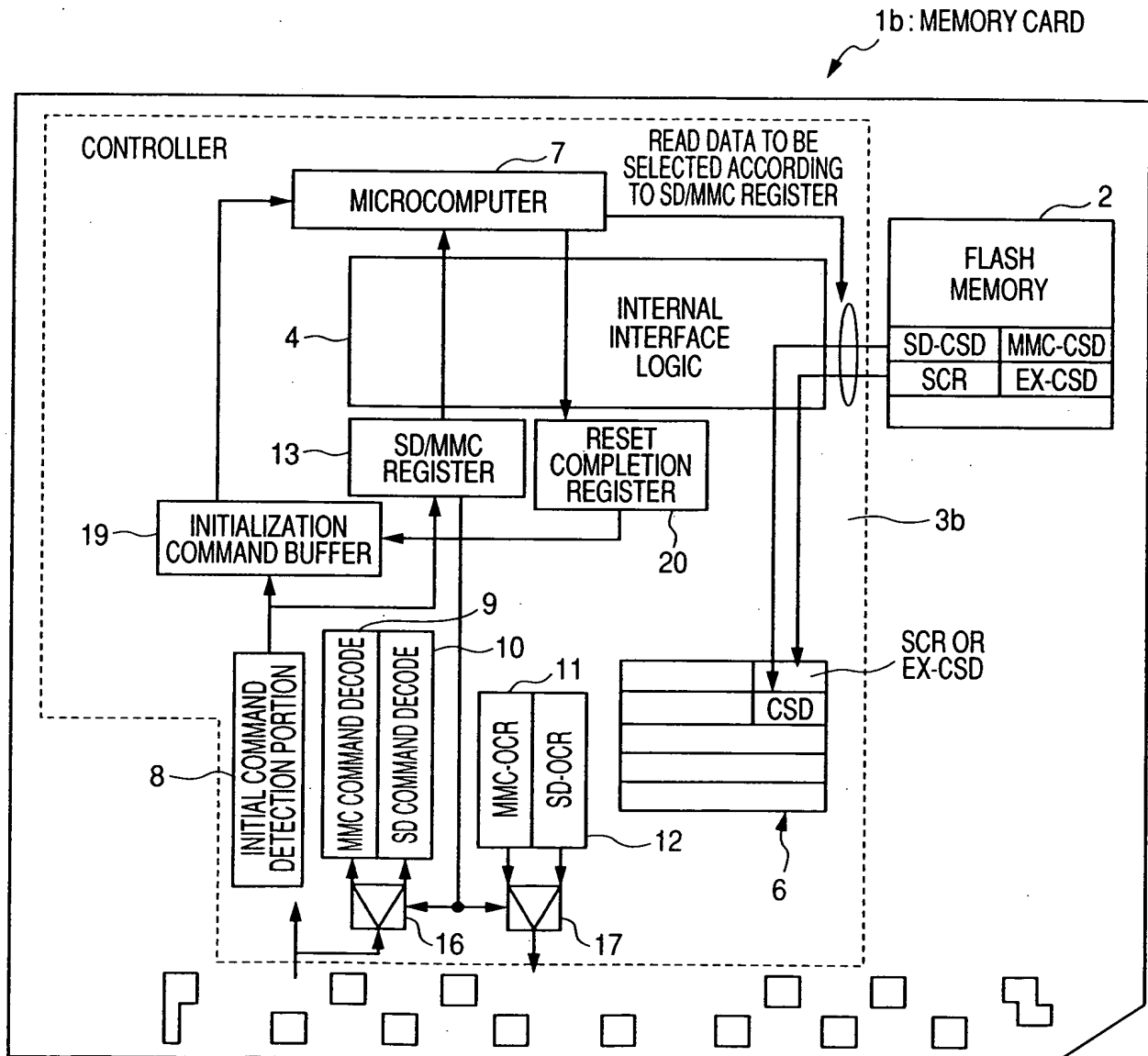




FIG. 9

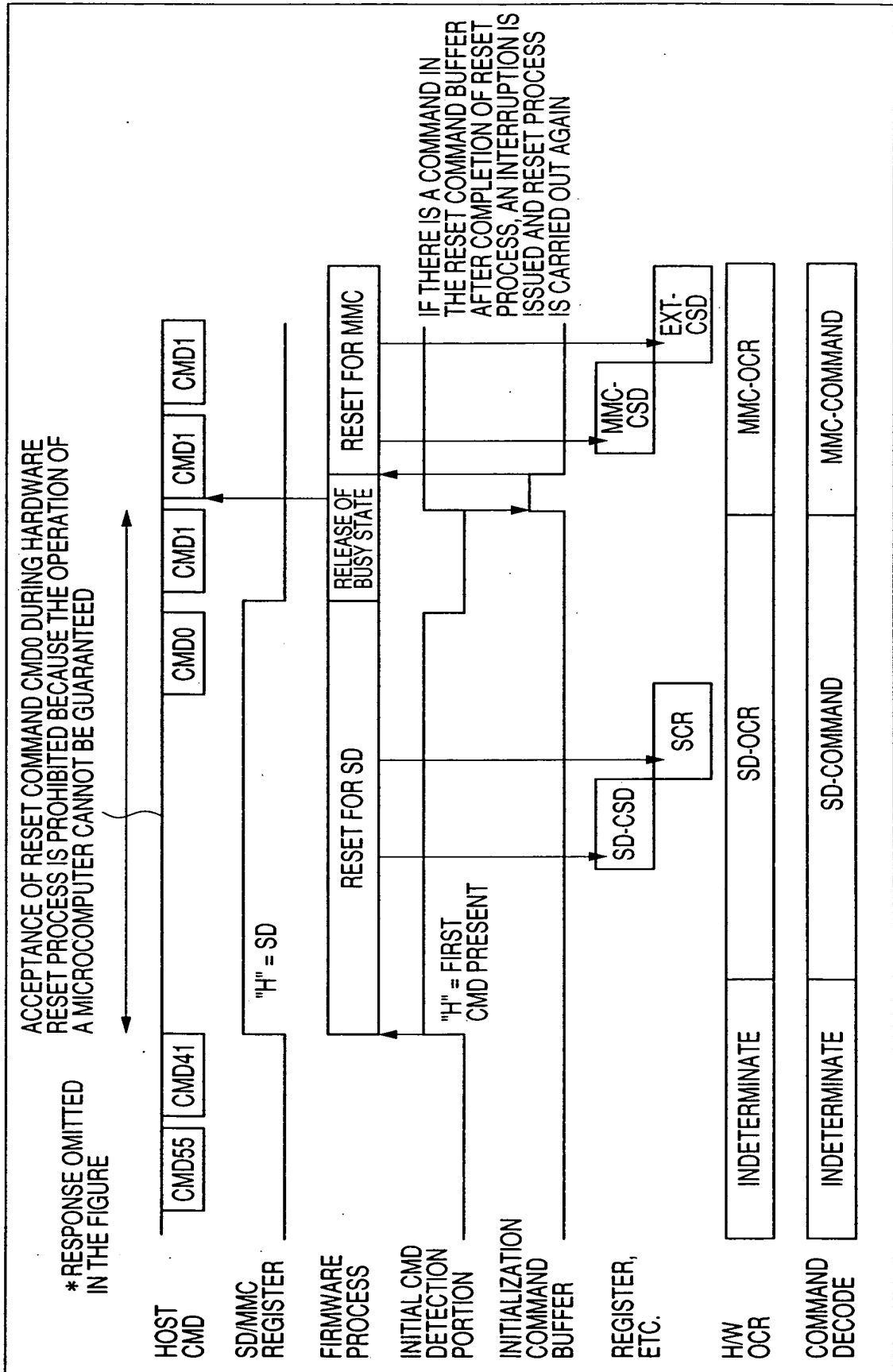


FIG. 10

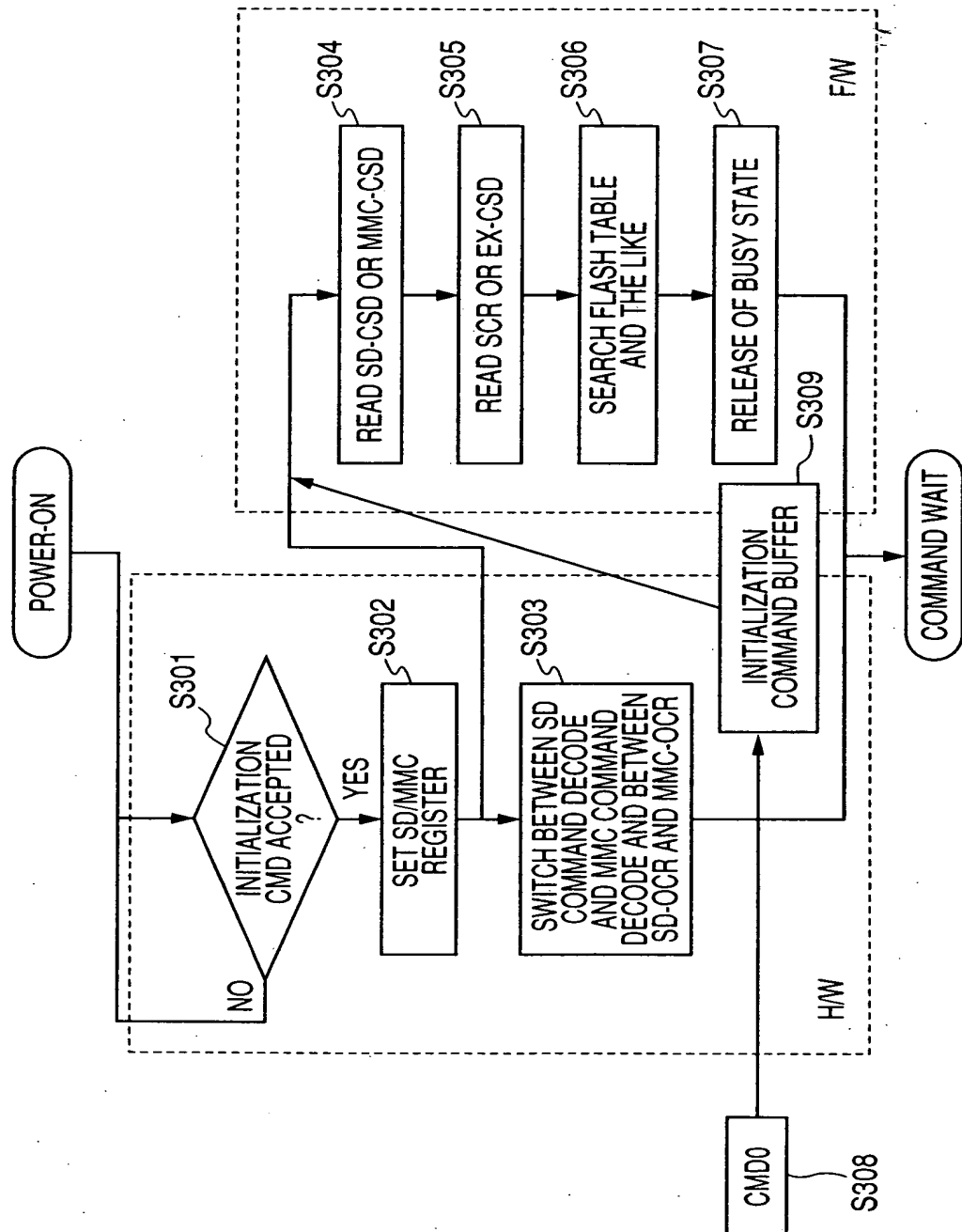


FIG. 11

DIFFERENCE	SD	MMC	INFLUENCE
INITIALIZATION COMMAND	CMD55+CMD41	CMD1	
CSD VALUE (bit [46:37])	GROUP ERASE PARAMETER DEFINITION	ERASE SECTOR/GROUP PARAMETER DEFINITION	HOST ERASE DEFINITION DIFFERS
COMMAND SUPPORT	ACMDxx FOR SD (CMD55+CMDxx)	SCMDyy FOR S-MMC (CMD55+CMDyy)	COMMAND DIFFERS
OCR VALUE (bit [7:0])	DEFINE VCC = 1.6 TO 2.0V AT bit [7:4]	DEFINE VCC = 1.65 TO 1.95V AT bit [7]	SUPPORT VOLTAGE DEFINITION DIFFERS
OTHER REGISTER	SCR REGISTER (512B) PROVIDED	EX-CSD REGISTER (512B) PROVIDED	SUPPORT FUNCTION AND STATUS CHECK METHOD DIFFER

FIG. 12

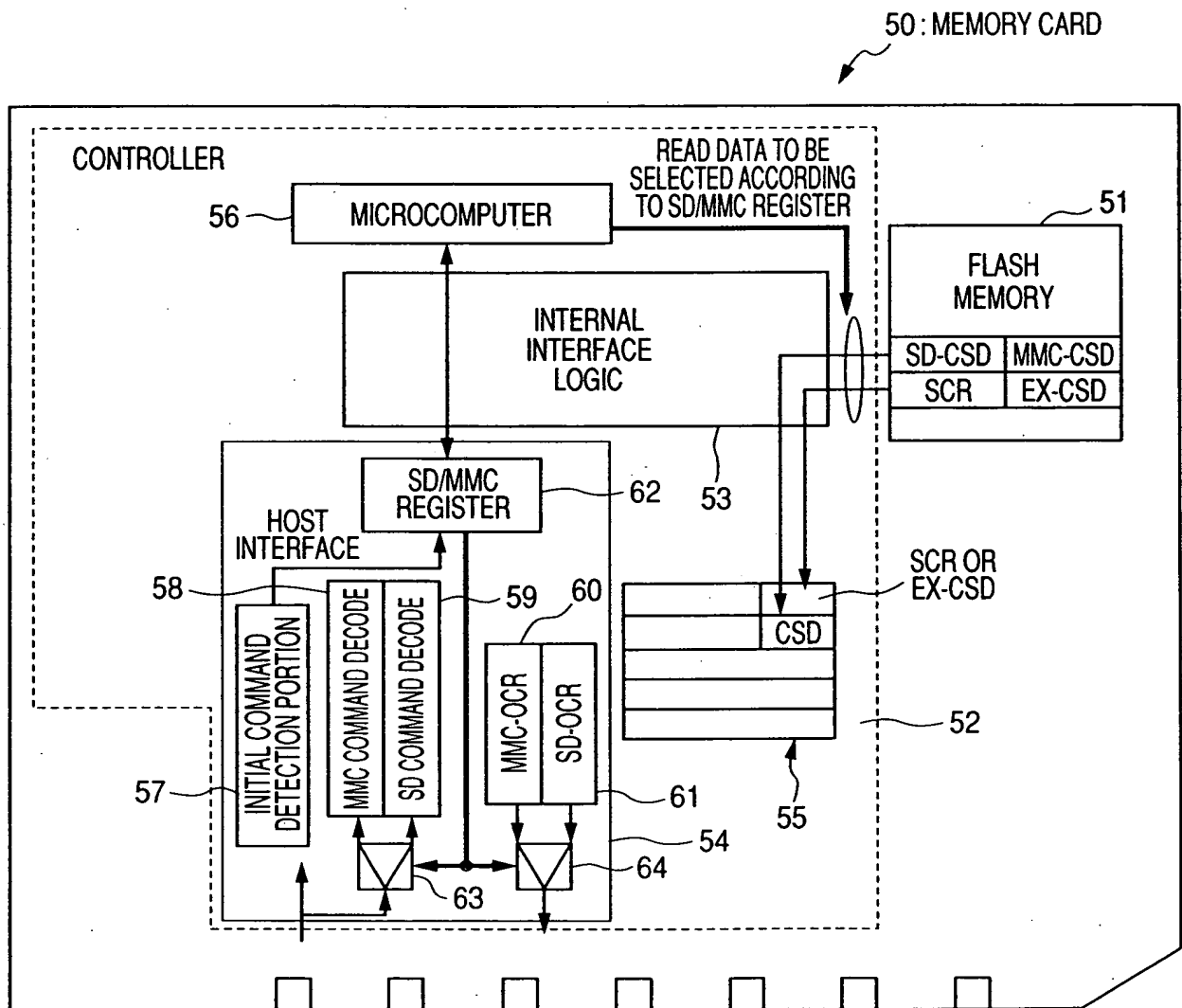


FIG. 13

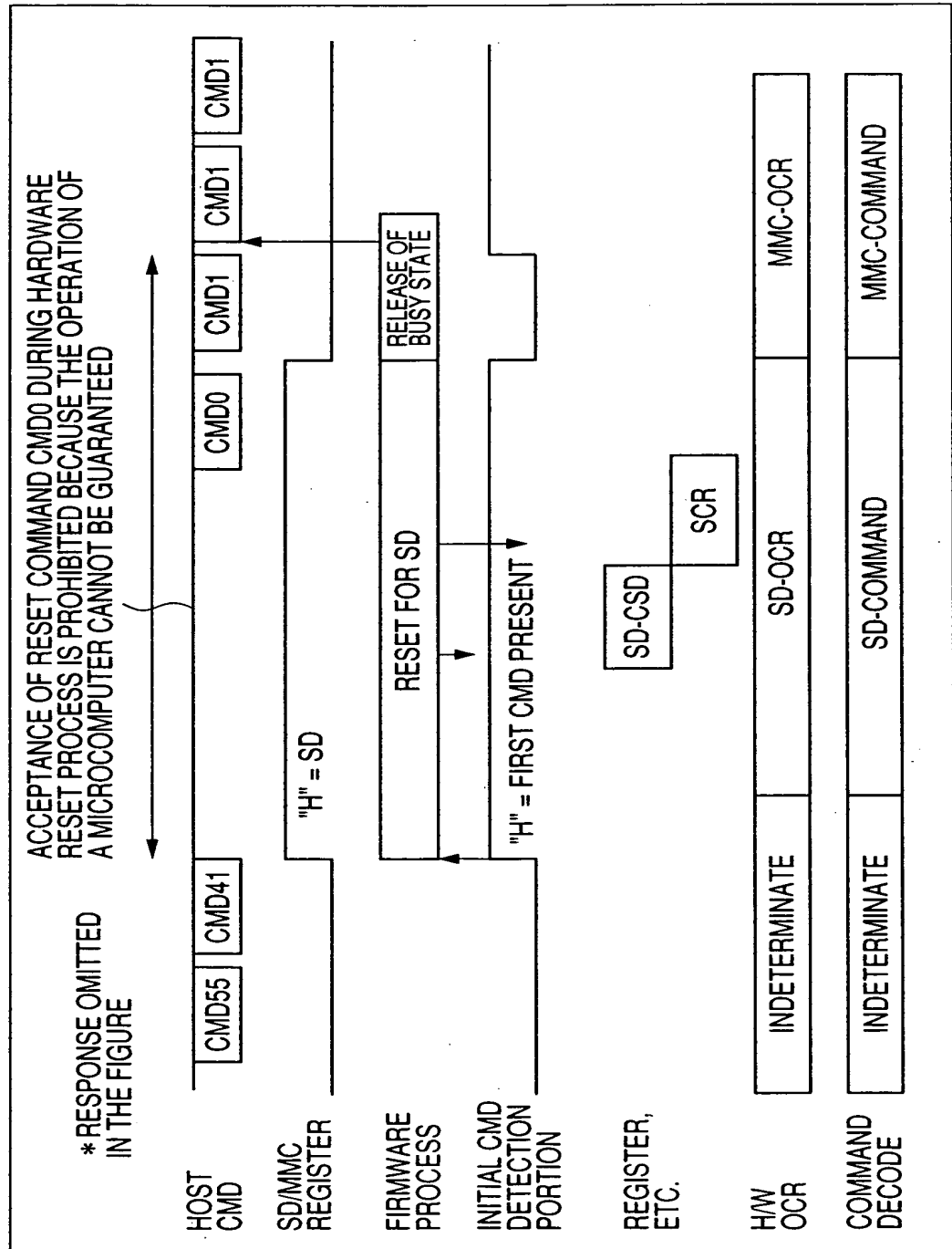


FIG. 14

